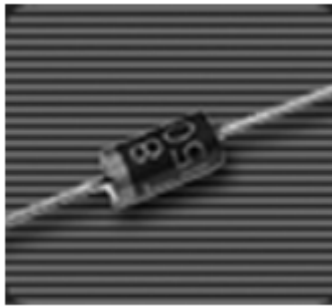


**Glass Passivated Junction Rectifier**
**DO-15(DO-204AC)**

**Features**

- ROHS compliant
- Glass passivated chip
- High forward surge capability
- Meet MSL level 1, per J-STD-020  
LF maximum peak of 250 °C
- Solder dip 260 °C / 40S
- Component in accordance to ROHS 2002/95/EC and WEEE 2002/96/WC
- UL recognition, file number E342874


**Primary characteristics**

$I_{F(AV)}$	2A
$V_{RRM}$	50V to 1000V
$I_{FSM}$	55A
$I_{RM}$	5 $\mu$ A
$V_{FM}$ at $I_F=2A$	1.1V
$T_J$ max.	150 °C

**Applications**

Ideal for ac-to-dc bridge full wave rectification such as SMPS, home appliances, office equipment, industrial automation applications

**Mechanical data**

- DO-15(DO-204AC)
- Epoxy meets UL 94 V-0 flammability rating
- Terminals: Tin plated leads.
- Polarity: As marked.
- Mounting Torque: 10cm·kg(8.8 inches·lbs)max.
- Recommended Torque: 5.7 cm·kg(5 inches·lbs)

**Maximum rating (Ta=25°C unless otherwise noted)**

Parameter	Sym	DO-15(DO-204AC)							Unit
		RL 201G	RL 202G	RL 203G	RL 204G	RL 205G	RL 206G	RL 207G	
Max. repetitive peak reverse voltage	$V_{RRM}$	50	100	200	400	600	800	1000	V
Max. RMS reverse voltage	$V_{RMS}$	35	70	140	280	420	560	700	V
Max. DC blocking voltage	$V_{DC}$	50	100	200	400	600	800	1000	V
Max. average forward current	$I_{F(AV)}$	2							A
Non-repetitive peak forward surge current 8.3ms single half-sine-wave	$I_{FSM}$	55							A
Max. instantaneous forward voltage drop per diode	$V_{FM}$	1.1 (2A)							V
Max. instantaneous reverse current at rated DC blocking voltage	$I_{RM}$	5							$\mu$ A
		100							$\mu$ A
Operating junction temperature	$T_J$	-55 ~ +150							°C
Storage temperature	$T_{STG}$	-55 ~ +150							°C
Typical junction capacitance (Note 1)	$C_J$	30							pF

**Notes:**

1. Measured at 1 MHz and Applied Reverse Voltage of 4.0 V D.C



**Ordering information (Example)**

PREFERRED	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
RL207G				

**Typical characteristics**

FIG.1- TYPICAL FORWARD CHARACTERISTICS

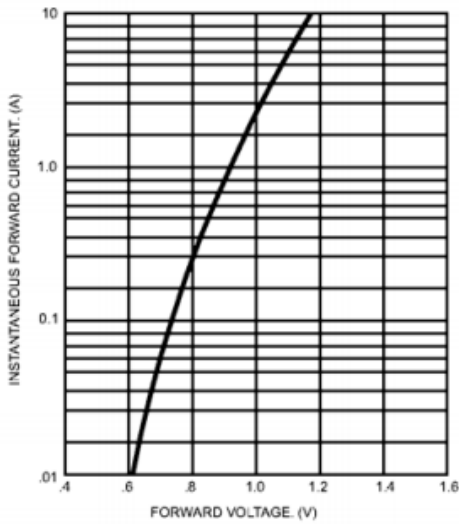


FIG.2- MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

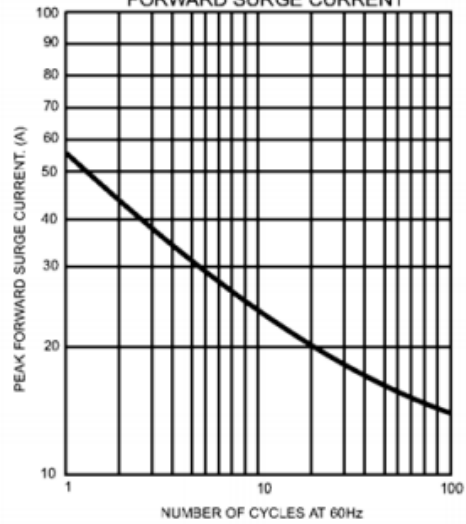


FIG.3- MAXIMUM FORWARD CURRENT DERATING CURVE

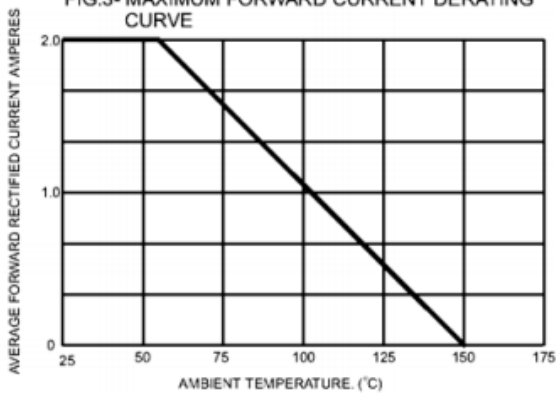
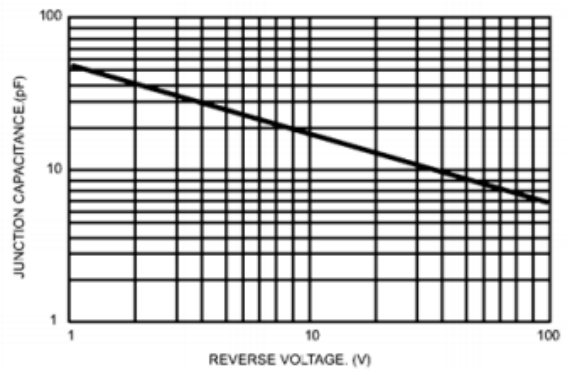
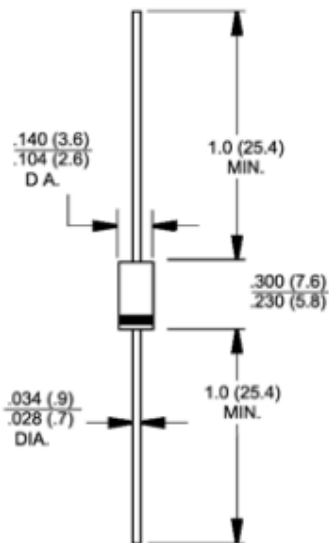


FIG.4- TYPICAL JUNCTION CAPACITANCE



Package outline dimensions



Dimensions in inches and (millimeters)

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